



REV A January 2011


Oscilent Controlled Document

Ordering Code / Part Number	Product Description
820-IF70.0M-JB	70.0 MHz IF SAW Filter 19.15 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

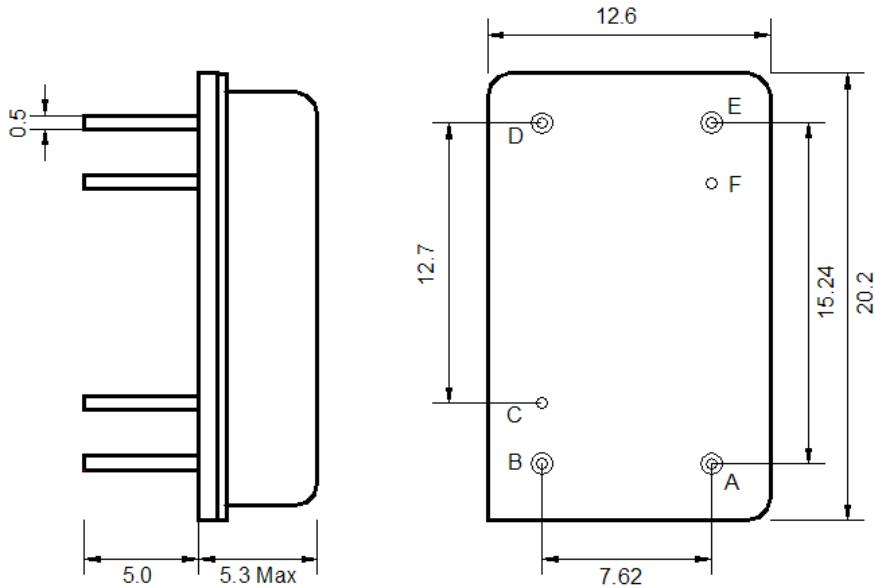
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



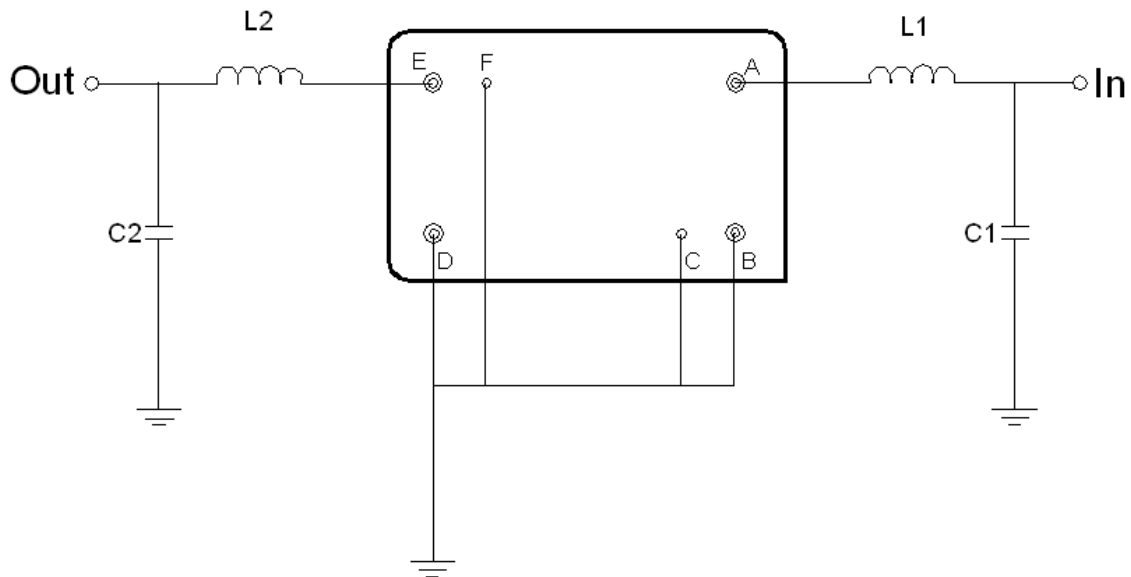


Mechanical Dimensions (mm)



Pin Description	
B, C, D, F	Ground
A	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1 = 150 nH, C1 = 15pF
Output	L2 = 180 nH, C2 = 15pF
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	70.0	-
Insertion Loss at Fo	dB	-	22.5	24.0
Amplitude Ripple Variation at Fo ±9.22 MHz	dB _{p-p}	-	0.7	1.0
Temperature Coefficient	ppm/°C	-	-72	-
Group Delay Variation at Fo ±9.22 MHz	nsec	-	50	100
Absolute Delay at Fo	µsec	-	2.42	-
Bandwidth at -1.0 dB	MHz	-	19.15	-
Bandwidth at -3.0 dB	MHz	19.2	19.47	-
Bandwidth at -50.0 dB	MHz	-	20.92	21.0
Relative Attenuation:				
Lower Sidelobe	dB	50	55	-
Upper Sidelobe	dB	50	55	-



Frequency Response

