



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
835-IF38.9M-F	38.9 MHz IF SAW Filter

### Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

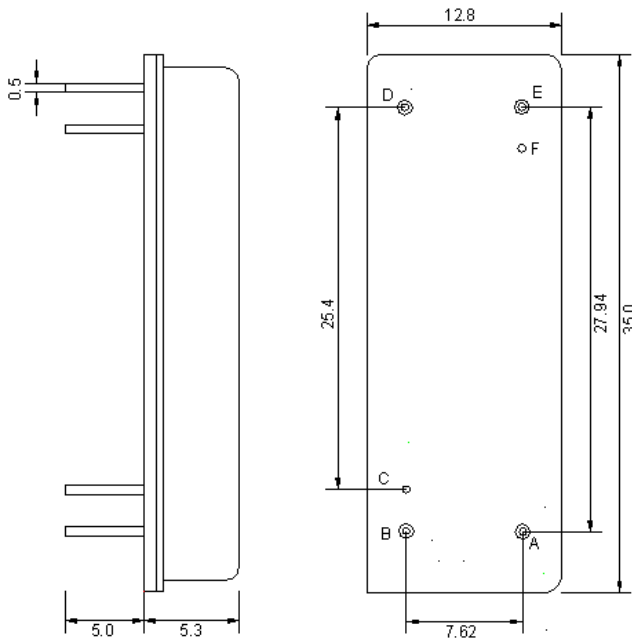
### Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



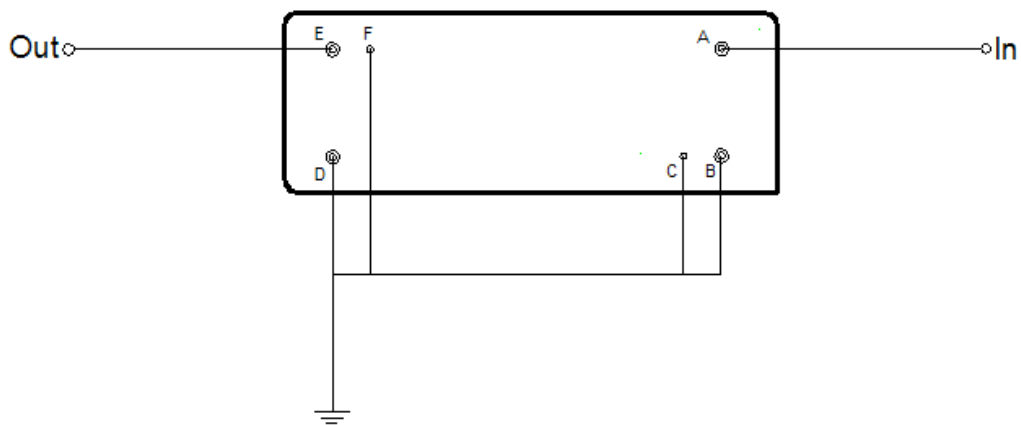


**Mechanical Dimensions (mm)**



Pin Description	
B, C, D, F	Ground
A	In
E	Out

**Test Circuit**



Source & Load Impedance: 50 Ω



## Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) <sup>(1)</sup>	Ω	-	50	-
Load Impedance (single ended) <sup>(1)</sup>	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

All Specifications at Tambient : +40°C

Operating Temperature Range: -20 to +70°C

## Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Nominal Frequency (Fo)	MHz	-	38.9	-
Insertion Loss at Fo	dB	-	30.0	31.5
Group Delay Variation (33.2~39.0 MHz) (smo 1%)	nsec	-	50	60
Absolute Delay at Fo	µsec	-	4.2	-
Temperature Coefficient	ppm/°C	-	-	94
Amplitude Ripple Variation (33.2~39.0 MHz)	dB <sub>p-p</sub>	-	0.5	0.7
Relative Attenuation				
~ 26.40MHz	dB	55	60	-
26.00MHz	dB	55	62	-
32.00MHz	dB	50	60	-
32.40MHz	dB	40	52	-
33.15MHz	dB	-0.5	0.1	0.5
39.40MHz	dB	-0.5	0.1	0.5
40.15MHz	dB	40	50	-
40.40MHz	dB	50	55	-
45.80MHz	dB	55	60	-
45.80MHz ~	dB	55	60	-
Reflected wave signal suppression 2.5µs~12.0µs after main pulse	dB	48	54	-



## Frequency Response

