



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
835-IF218.64M-A	:18.64 MHz IF SAW Filter 1.72MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

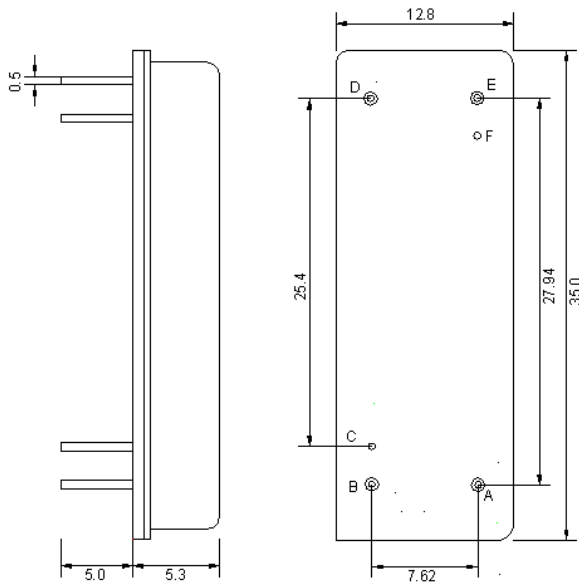
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



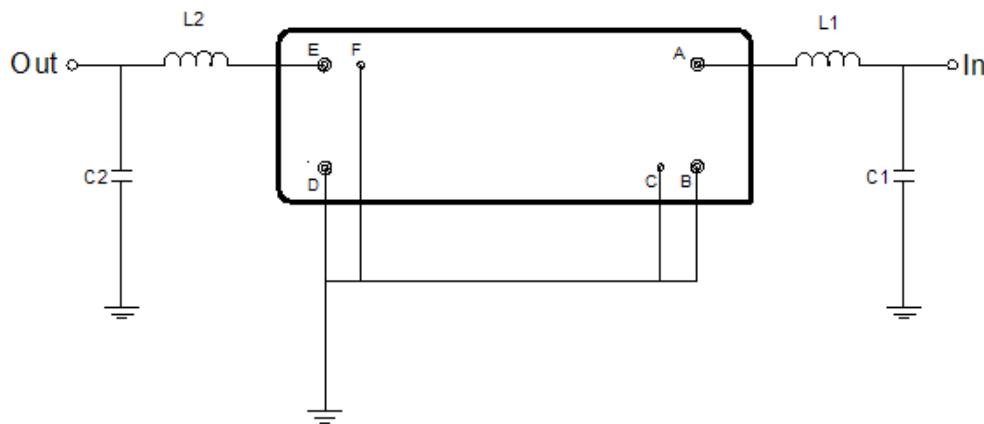


Mechanical Dimensions (mm)



Pin Description	
B, C, D, F	Ground
A	In
E	Out

Test Circuit



Test Fixture & Values	
Input	L1=12nH, C1=30pF
Output	L2=15nH, C2=36pF
Source/Load Impedance	50 Ω

**Maximum Ratings**

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	218.640	-
Insertion Loss at Fo	dB	-	23.2	25.0
Amplitude Ripple Variation	dB _{p-p}	-	0.75	1.2
Group Delay Variation (smo 0.5%)	nsec	-	180	-
Absolute Delay at Fo	μsec	-	3.97	-
Temperature Coefficient	ppm/°C	-	-0.03	-
Bandwidth at -1.0 dB	MHz	-	1.72	-
Bandwidth at -3.0 dB	MHz	-	1.87	-
Bandwidth at -40.0 dB	MHz	-	2.54	-
Ultimate Rejection	dB	50	55	-



Frequency Response

