



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-SL70.0M-16	70.0 MHz IF SAW Filter 15.45 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response
- o Smith Chart
- o VSWR

Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



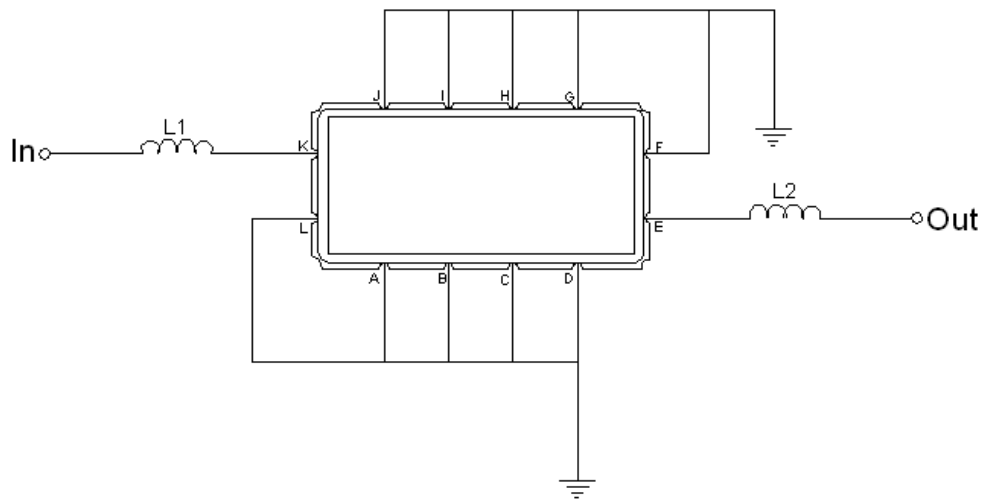


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=220nH Q > 40
Output	L2=220nH Q > 40
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	69.8	70.0	70.2
Insertion Loss at Fo	dB	-	12.2	13.2
Temperature Coefficient	ppm/°C	-	-84	-
Amplitude Ripple Variation at Fo ± 7.0 MHz	dB _{p-p}	-	0.5	1.0
Group Delay Variation at Fo ±7.0 MHz	nsec	-	50	80
Absolute Delay at Fo	µsec	-	0.88	-
IN/OUT Return Loss at Fo	dB	-	-	-
Bandwidth at -1.0 dB	MHz	15.2	15.45	-
Bandwidth at -3.0 dB	MHz	16.0	16.42	-
Bandwidth at -40.0 dB	MHz	-	20.17	21.0
Relative Attenuation:				
from 10 to 59.5 MHz	dB	40	43	-
from 82 to 140 MHz	dB	40	43	-



Frequency Response

