



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
820-SL70.0M-06	70.0 MHz IF SAW Filter 5.6 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

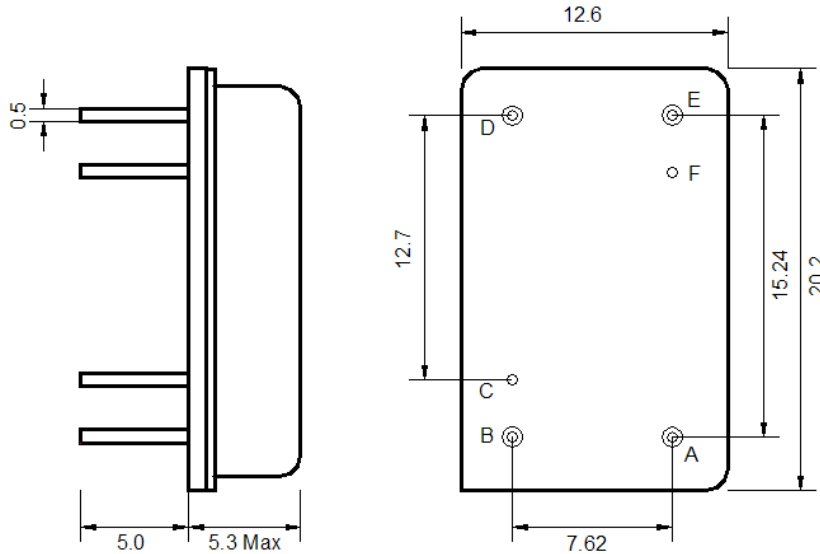
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



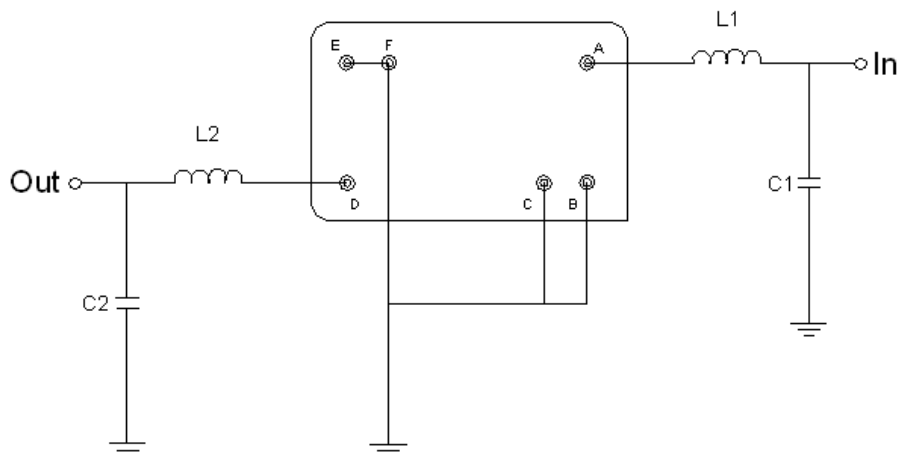


Mechanical Dimensions (mm)



Pin Description	
B, C, D, F	Ground
A	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=TBD Q > 40, C1=TBD
Output	L2=TBD Q > 40, C2=TBD
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	69.8	70.0	70.2
Insertion Loss at Fo	dB	-	8.2	9.0
Temperature Coefficient	ppm/°C	-	-94	-
Amplitude Ripple Variation at Fo ± 2.1 MHz	dB _{p-p}	-	0.7	1.0
Group Delay Variation at Fo ± 2.0 MHz	nsec	-	110	150
Absolute Delay at Fo	µsec	-	0.85	-
IN/OUT Return Loss at Fo	dB	-	-	-
Bandwidth at -1.0 dB	MHz	5.2	5.6	-
Bandwidth at -3.0 dB	MHz	6.0	6.5	-
Bandwidth at -40.0 dB	MHz	-	9.5	10.0
Relative Attenuation:				
from 10 to 64.5 MHz	dB	40	45	-
from 75.5 to 140 MHz	dB	40	43	-



Frequency Response

