



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
820-IF62.5M-19D	62.50MHz IF SAW Filter 19.10 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
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- o VSWR

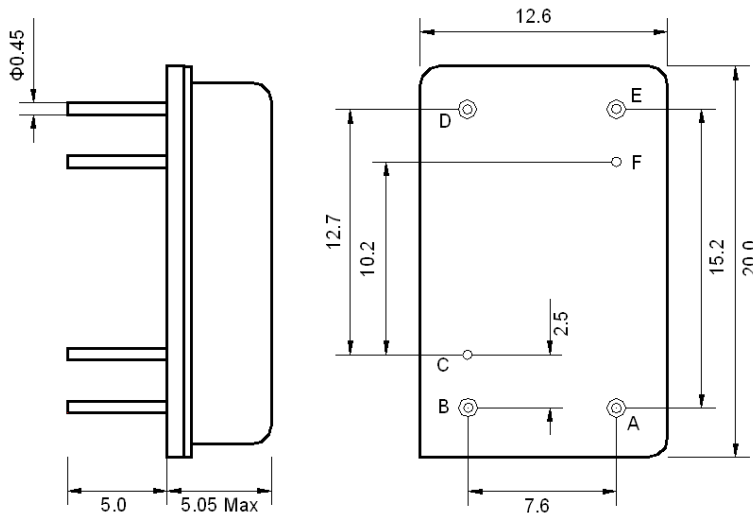
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



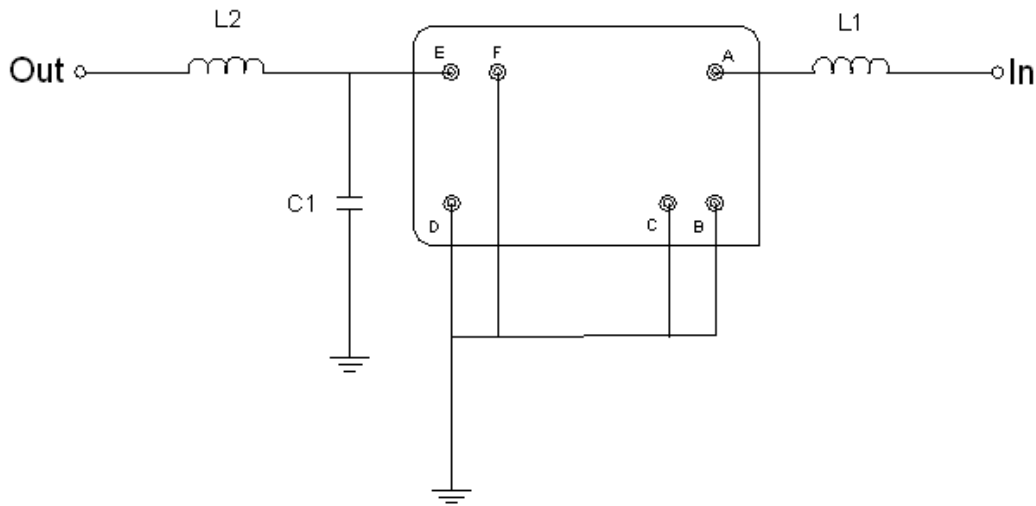


Mechanical Dimensions (mm)



Pin Description	
B, C, D, F	Ground
A	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=220 nH
Output	L2=220 nH, C1=3.6pF
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-	25	-
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

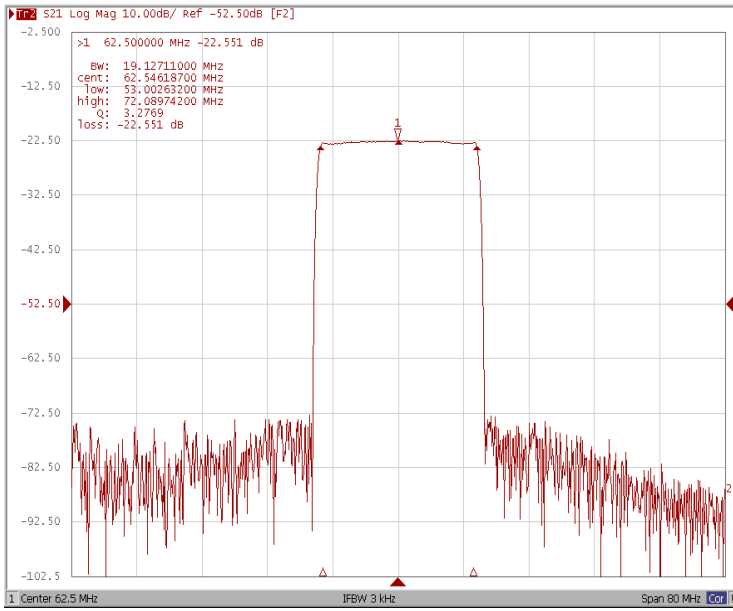
Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	62.5	-
Insertion Loss at Fo	dB	-	22.5	24.0
Group Delay Variation (Fo±9.22MHz)	ns	-	35	80
Absolute Delay Time at Fo	us	-	2.33	-
Temperature Coefficient	ppm/°C	-	-72	-
Amplitude Ripple (Fo±9.22MHz)	dB	-	0.65	1.00
Bandwidth at -1dB	MHz	-	19.10	-
Bandwidth at -3dB	MHz	19.20	19.50	-
Bandwidth at -50dB	MHz	-	20.93	21.00
Ultimate Rejection	dB	50	53	-

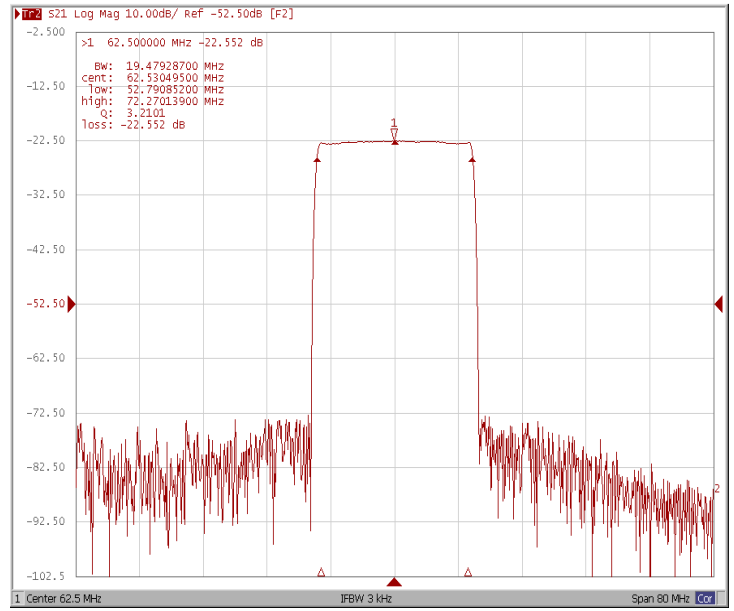


Frequency Response

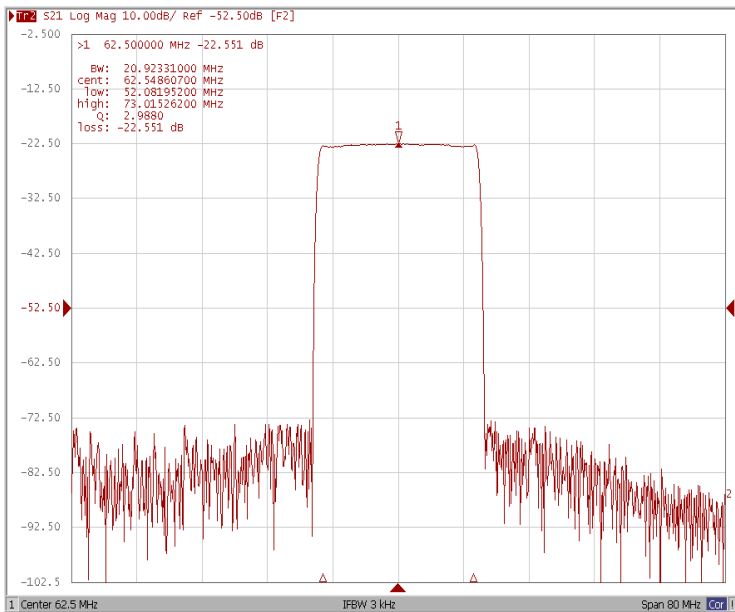
Bandwidth at -1.0 dB



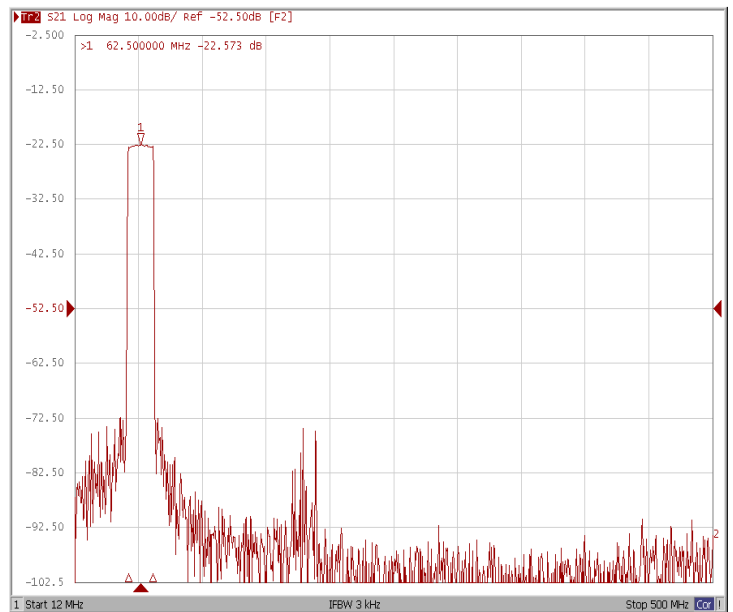
Bandwidth at -3.0 dB



Bandwidth at -50.0 dB

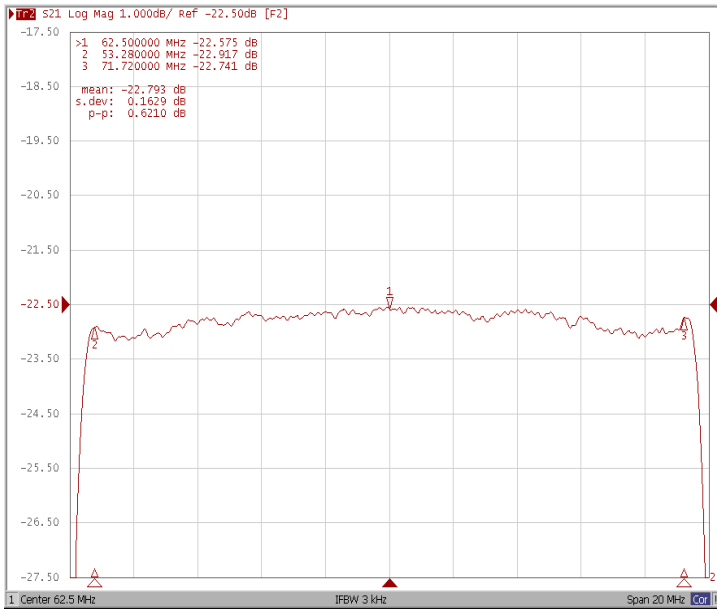


Wide_Band

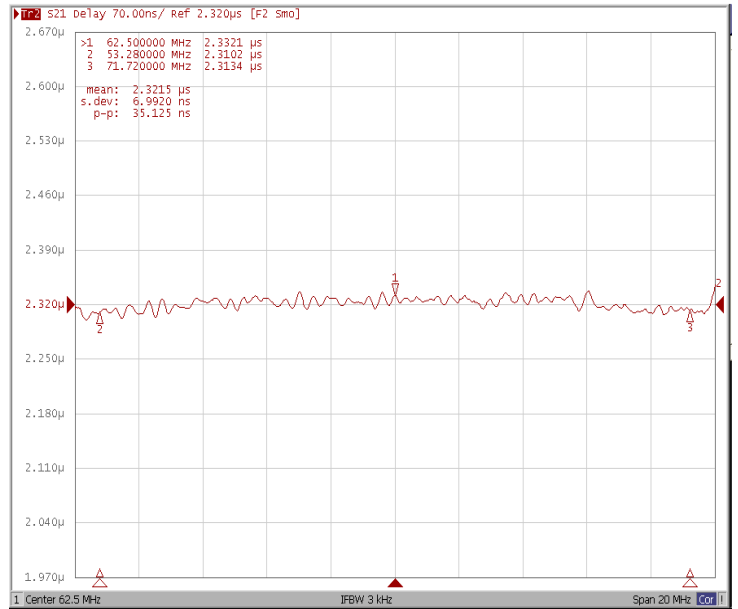




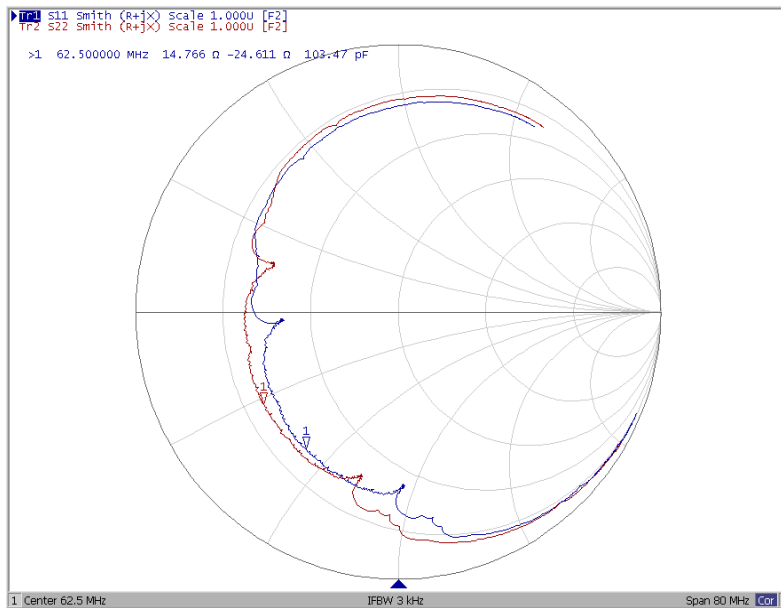
Ripple Variation Fo±9.22MHz



Group Delay Variation Fo±9.22MHz



Smith Chart





VSWR

