



PRODUCT SPECIFICATION

REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-SL75.0M-30	Low-Loss 75MHz IF SAW Filter

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Electrical Specification
- o Frequency Response

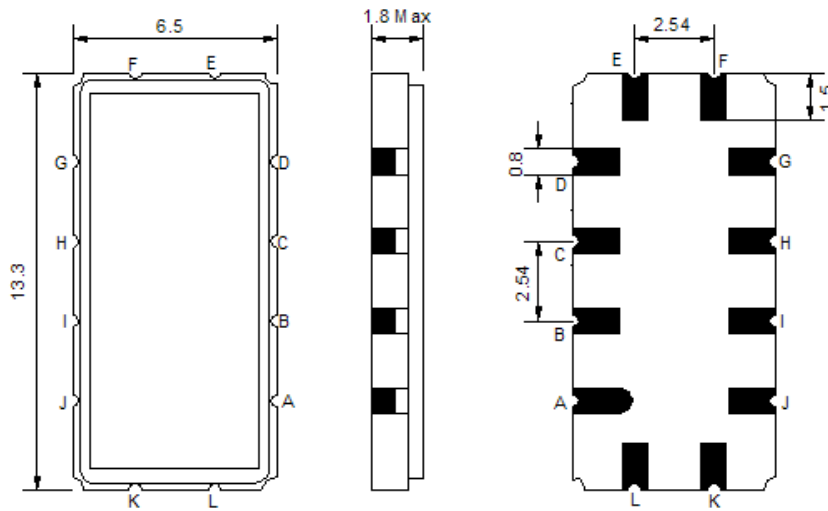
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



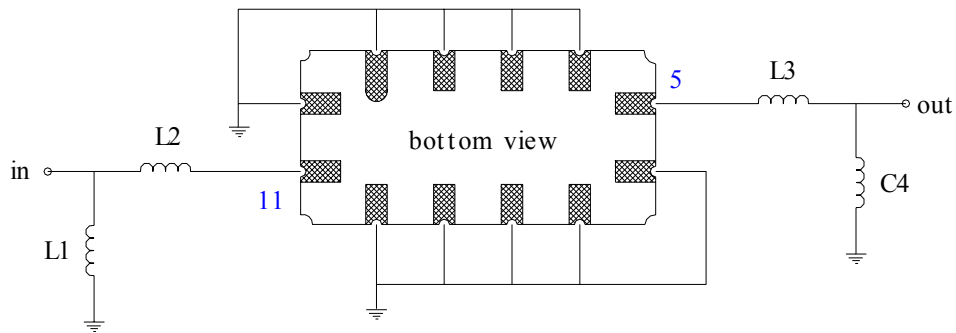


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=150nH, L2=180nH
Output	L3=150nH, L4=150nH
Source/Load Impedance	50 Ω

**Electrical Specification**

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	75	-
Insertion Loss at Fo	dB	-	17.0	18.5
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Temperature Coefficient	ppm/°C	-	-94	-
Amplitude Ripple within fo \pm 13.5 MHz	dB _{p-p}	-	0.7	1.0
Bandwidth at -1.0 dB	MHz	29.0	29.9	-
Bandwidth at -3.0 dB	MHz	30.0	31.0	-
Bandwidth at -40.0 dB	MHz	-	35.2	37.0
Relative Attenuation:				
Lower sidelobe	dB	40	45	-
Upper sidelobe	dB	40	45	-
Group Delay Variation within fo \pm 13.5 MHz	ns	-	30	60
Absolute Delay at Fo	μ s	-	0.86	0.9
IN/OUT Return Loss at Fo	dB	-	-	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.



Frequency Response

