



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-SL60.0M-10	60.0MHz IF SAW Filter 23.55MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

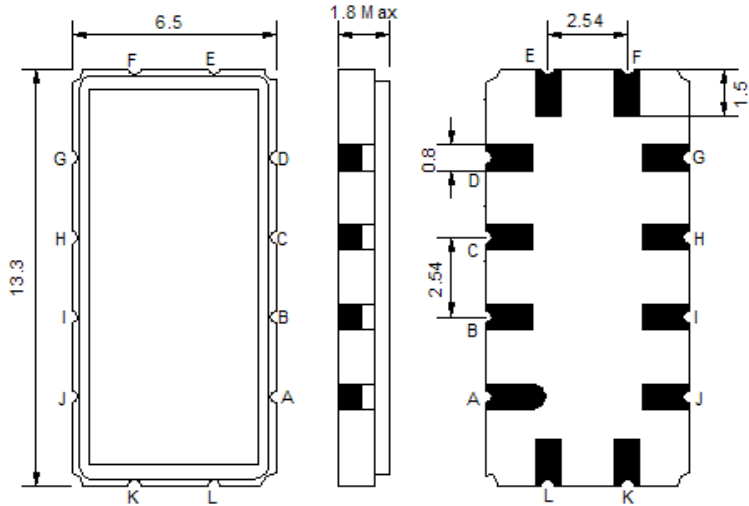
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



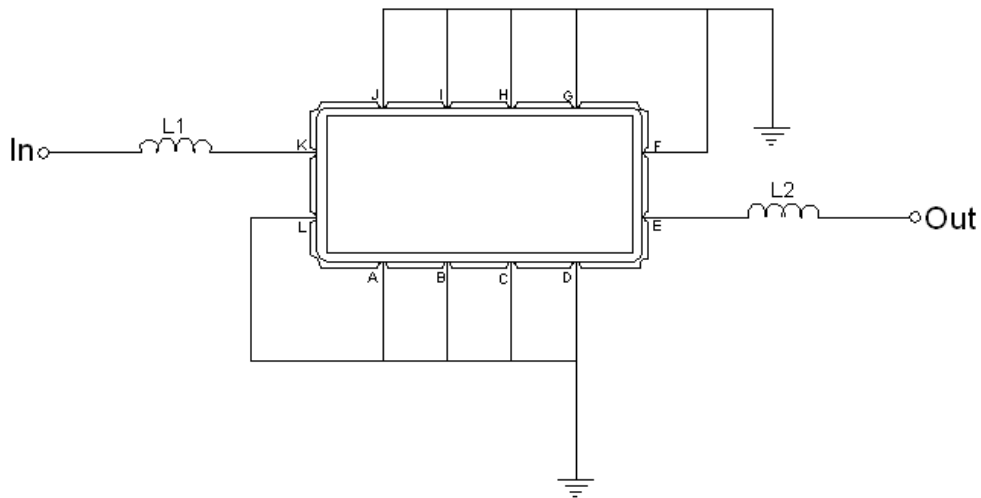


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=270 nH, Q>40
Output	L2=270 nH, Q>40
Source/Load Impedance	50 Ω

**Maximum Ratings**

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	60.0	-
Insertion Loss at Fo	dB	-	10.5	12.0
Amplitude Ripple Variation	dB _{p-p}	-	0.6	1.0
Group Delay Variation	nsec	-	80	150
Absolute Delay at Fo	μsec	-	1.1	-
Temperature Coefficient	ppm/°C	-	-94	-
Bandwidth at -1.0 dB	MHz	10.0	10.3	-
Bandwidth at -3.0 dB	MHz	-	11.0	-
Bandwidth at -40.0 dB	MHz	-	13.5	14.0
Relative Attenuation:				
10 ~ 52.5 MHz	dB	40	-	-
67.5 ~ 120 MHz	dB	40	-	-
IN/OUT Return Loss at Fo	dB	-	-	-



Frequency Response

