PRODUCT SPECIFICATION

REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-SL130.9M-07A	130.9 MHz IF SAW Filter 7.65 MHz Bandwidth

Specification Contents

- Mechanical Dimensions
- Test Circuit
- Maximum Ratings
- Electrical Specification
- Frequency Response
- Smith Chart
- VSWR

Notes

- Electrostatic Sensitive Device (ESD)
- Avoid excessive ultrasonic exposure
- Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- This product complies with EU directive 2002/95/EC (RoHS compliance)

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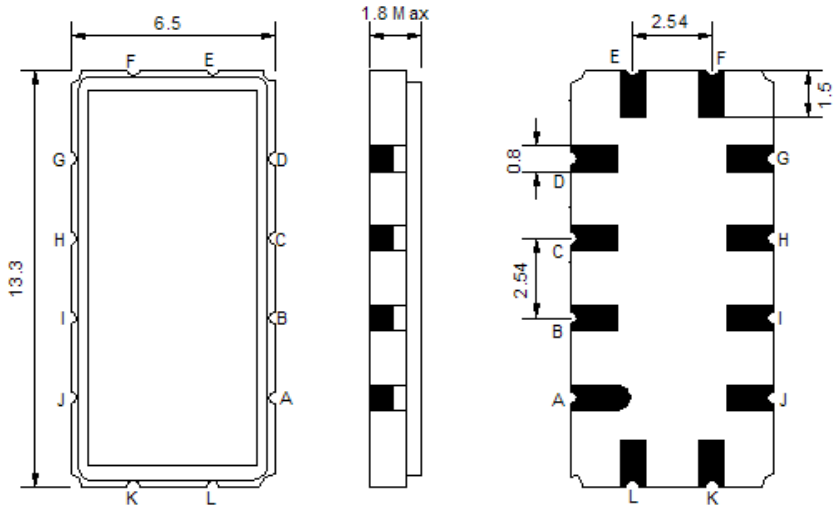
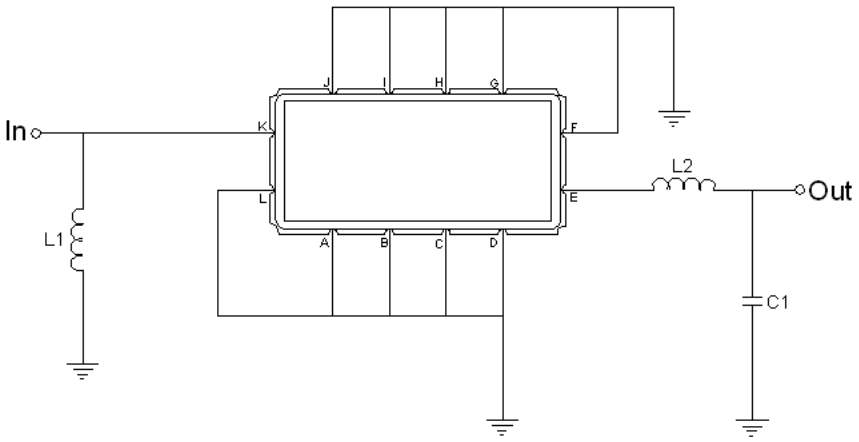
130.9 MHz IF SAW Filter 7.65 MHz Bandwidth

Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=39 nH
Output	L2=100 nH, C2=20 pF
Source/Load Impedance	50 Ω

Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-20	-	70
Storage Temperature Range	°C	-40	-	80
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended)(1)	Ω	-	50	-
Load Impedance (single ended)(1)	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	130.9	-
Insertion Loss at Fo	dB	-	16.7	20.0
Amplitude Ripple Variation	dB_{p-p}	-	0.4	0.8
Group Delay Variation at Fo ± 3.5 MHz	nsec	-	40	80
Absolute Delay at Fo	µsec	-	0.85	-
Temperature Coefficient	ppm/°C	-	-23	-
Bandwidth at –1.0 dB	MHz	7.4	7.65	-
Bandwidth at –3.0 dB	MHz	8.2	8.45	-
Bandwidth at –40.0 dB	MHz	-	11.95	12.5
Relative Attenuation				
Lower Sidelobe	dB	43	48	-
Upper Sidelobe	dB	43	48	-

Frequency Response