



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
820-IF70.0M-Y	70.0 MHz IF SAW Filter 11.9 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

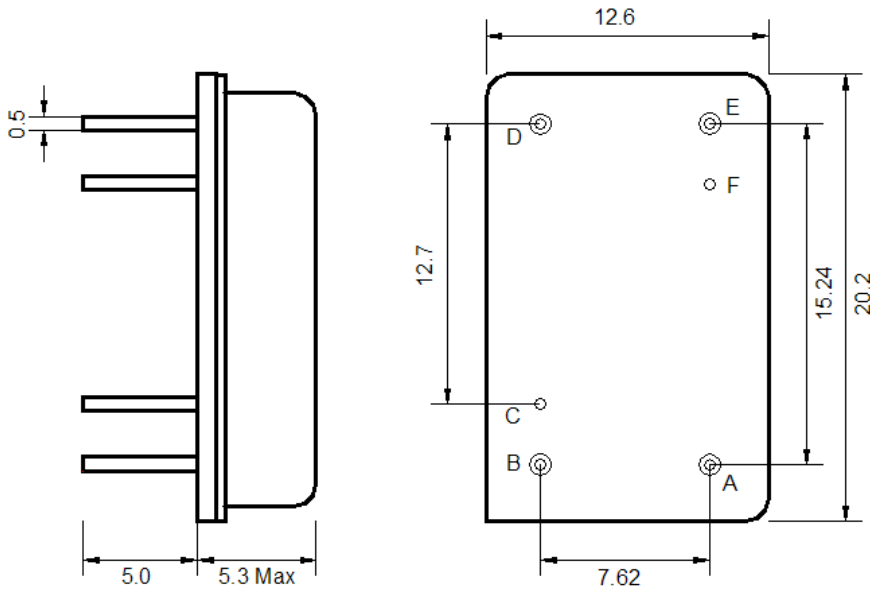
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



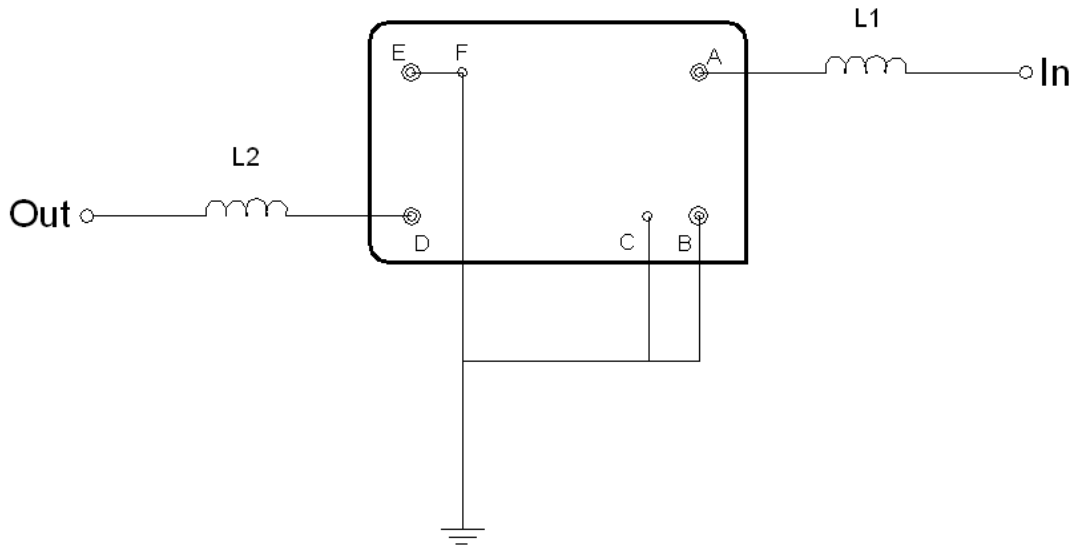


Mechanical Dimensions (mm)



Pin Description	
B, C, D, F	Ground
A	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1 = 150 nH
Output	L2 = 3.3 nH
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

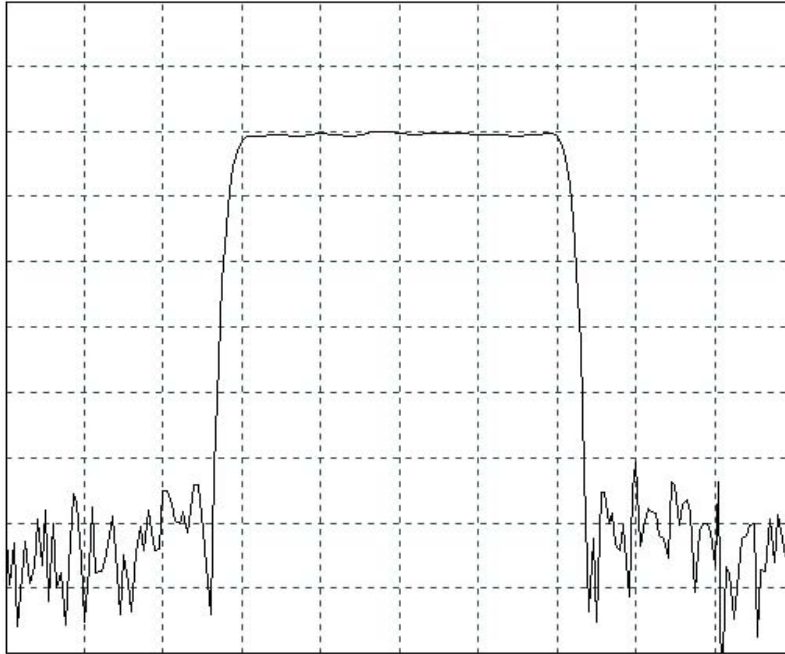
Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	69.9	70.0	70.1
Insertion Loss at Fo	dB	-	23.0	26.0
Group Delay Variation	nsec	-	40	70
Absolute Delay at Fo	usec	-	2.28	-
Phase Linearity	deg	-	4.5	7.0
Passband Ripple Variation	dB	-	0.8	1.0
Bandwidth at -1dB	MHz	11.6	11.9	-
Bandwidth at -3dB	MHz	12.1	12.4	-
Bandwidth at -40dB	MHz	-	14.0	14.5
Ultimate Rejection	dB	50	55	-
Temperature Coefficient	ppm/°C	-	-84	-

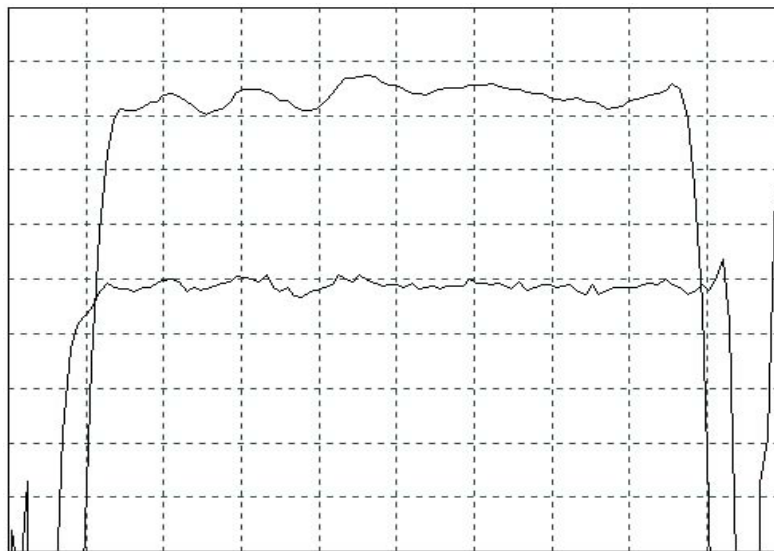


Frequency Response



Horizontal: 3.0 MHz/Div

Vertical: 10 dB/Div



Horizontal: 1.6 MHz/Div

Vertical: 1.0 dB/Div

Vertical: 100 ns/Div