



PRODUCT SPECIFICATION

REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
820-IF70.0M-R	70.0MHz IF SAW Filter 0.3 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response

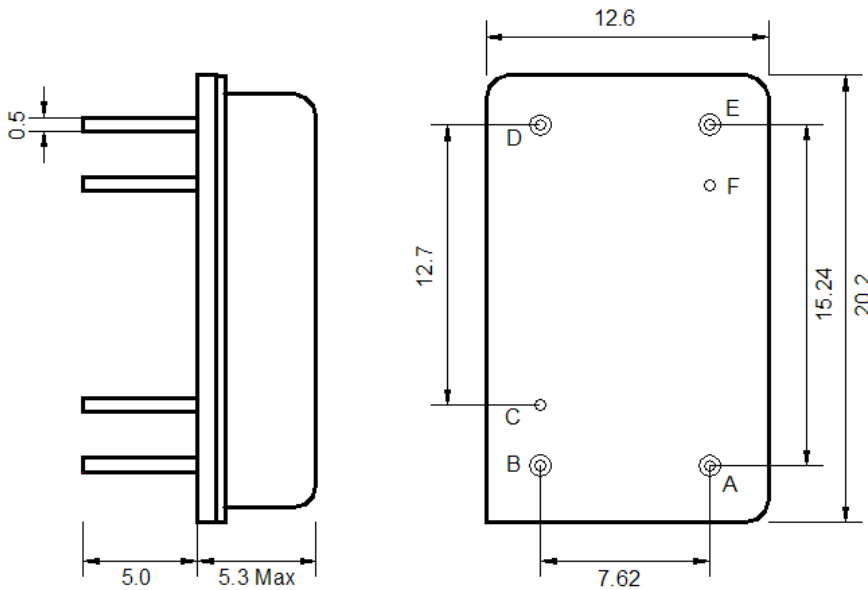
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



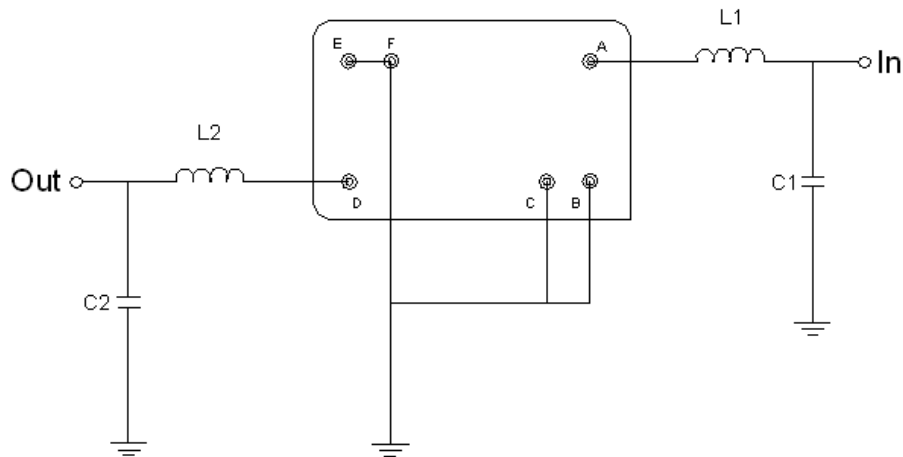


Mechanical Dimensions (mm)



Pin Description	
B, C, E, F	Ground
A	Input
D	Output

Test Circuit



Test Fixture & Values	
Input	L1 = 180 nH, C1 = 62pF
Output	L2 = 180 nH, C2 = 62pF
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-55	-	85
Storage Temperature Range	°C	-55	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

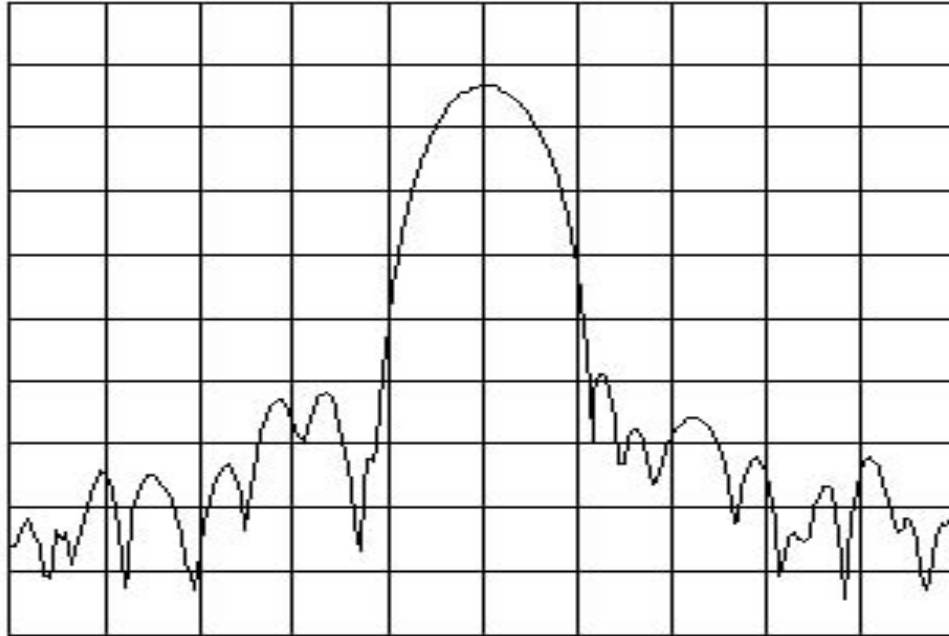
Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

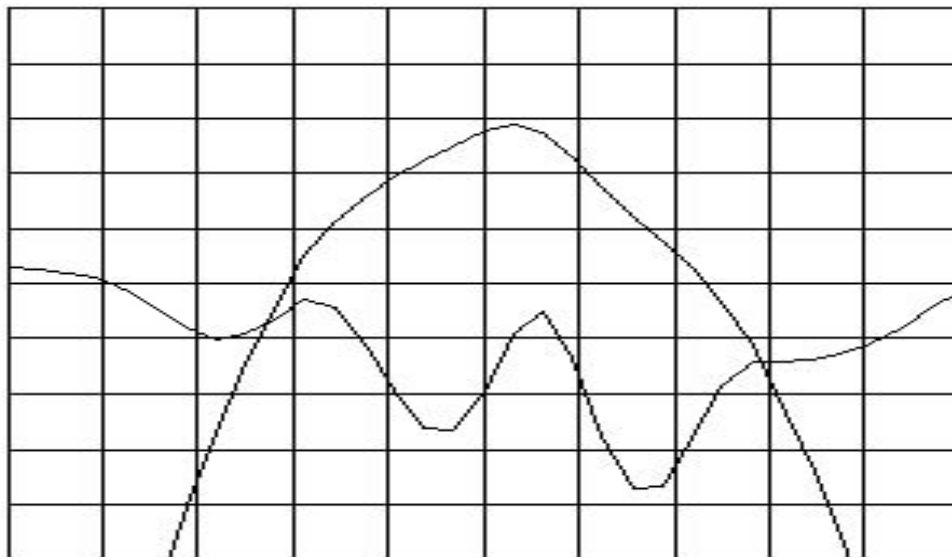
Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	69.9	70.0	70.1
Insertion Loss at Fo	dB	-	13.5	16.0
Amplitude Ripple Variation at Fo±100KHz	dB _{p-p}	-	1.2	1.5
Group Delay Variation	nsec	-	300	1000
Absolute Delay at Fo	μsec	-	2.63	-
Temperature Coefficient	ppm/°C	-	-0.03	-
Bandwidth at -1.0 dB	MHz	0.12	0.18	-
Bandwidth at -3.0 dB	MHz	0.20	0.34	-
Bandwidth at -40.0 dB	MHz	-	1.10	1.20
Ultimate Attenuation	dB	60	65	-
Ultimate Rejection	dB	45	50	-



Frequency Response



Horizontal: 0.5 MHz/Div
Vertical: 10 dB/Div



Horizontal: 0.8 MHz/Div
Vertical: 1 dB/Div
Vertical: 100nsec/Div