PRODUCT SPECIFICATION

REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
813-IF62.5M-A	62.5 MHz IF SAW Filter 20.25 MHz Bandwidth

Specification Contents

Notes

- Electrostatic Sensitive Device (ESD)
- Avoid excessive ultrasonic exposure
- Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- This product complies with EU directive 2002/95/EC (RoHS compliance)

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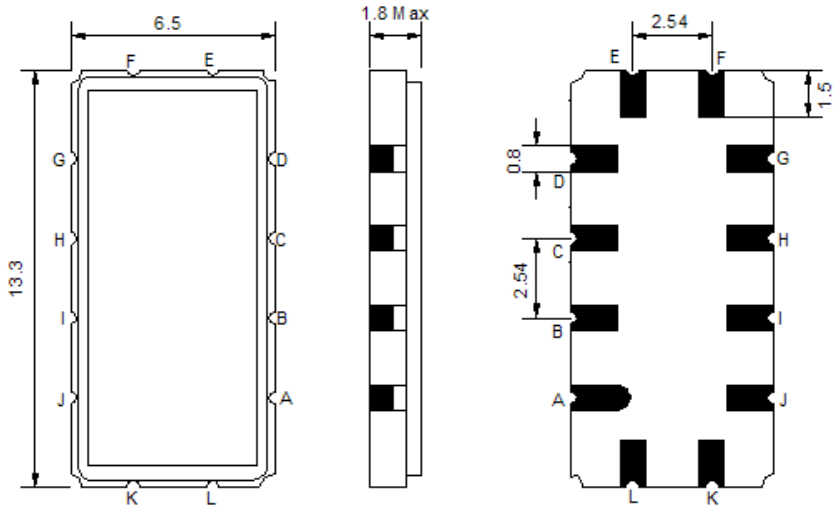
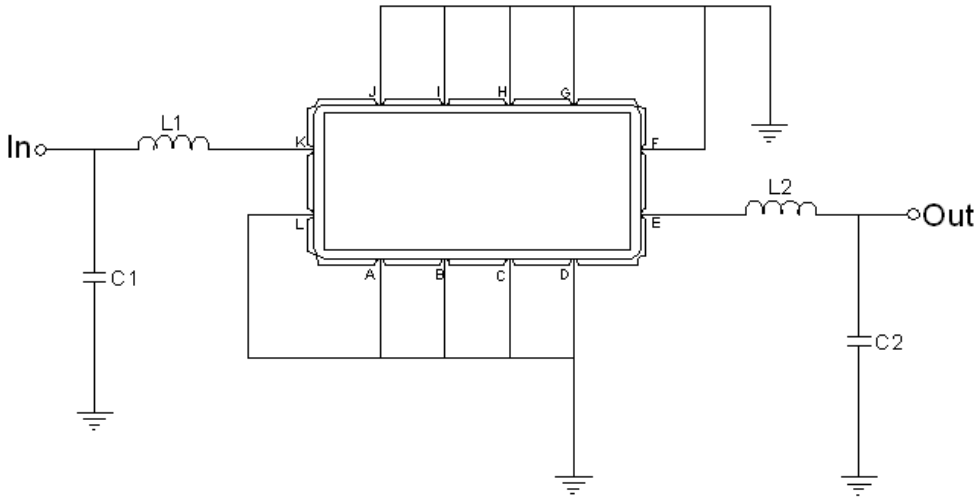
Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1 = 150 nH, C1 = 10 pF
Output	L2 = 470 nH, C2 = 13 pF
Source/Load Impedance	50 Ω

Maximum Ratings

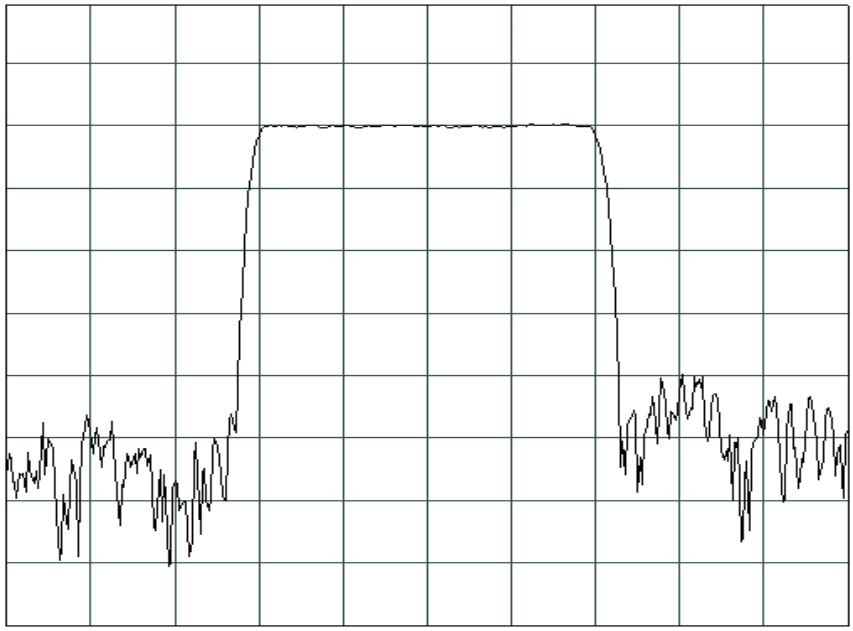
Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-30	-	80
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended)(1)	Ω	-	50	-
Load Impedance (single ended)(1)	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).
Those impedances could be modified with different impedance values and/or structures, if necessary.

Electrical Specification

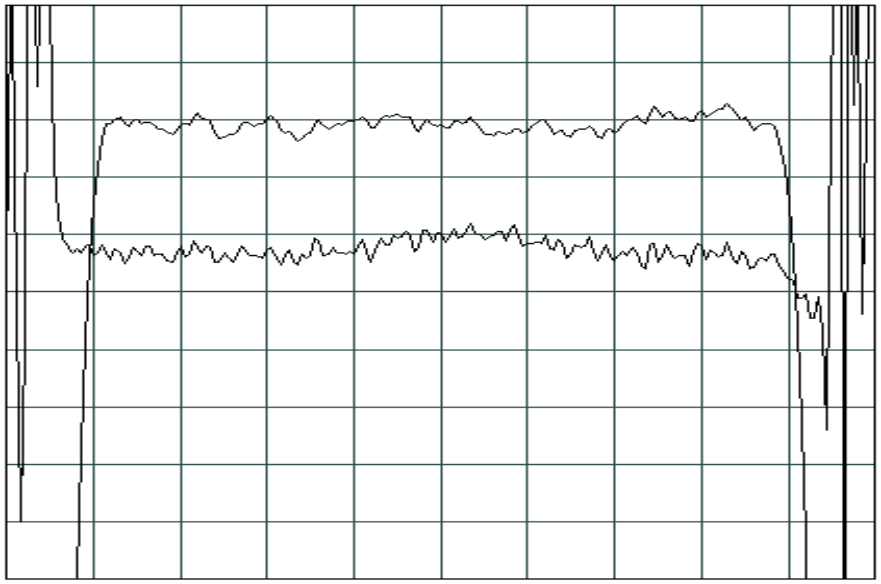
Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	62.42	62.50	62.58
Insertion Loss at Fo	dB	-	20.5	23.0
Group Delay Variation	nsec	-	70	120
Absolute Delay at Fo	usec	-	1.1	-
Passband Ripple Variation	dB	-	0.65	1.0
Bandwidth at -1dB	MHz	19.75	19.85	-
Bandwidth at -3dB	MHz	20.25	20.35	-
Bandwidth at -40dB	MHz	-	22.5	23.5
Ultimate Rejection	dB	40	45	-
Substrate Material		-	LN	-
Ambient Temperature	°C	-	25	-

Frequency Response



Horizontal: 5.0 MHz/Div

Vertical: 10 dB/Div



Horizontal: 2.5 MHz/Div

Vertical: 1 dB/Div

Vertical: 100 ns/Div